

SEMICONDUCTOR LASER DEVICE AND METHOD OF FABRICATING SAME

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BACKGROUND OF THE INVENTION

(a) Field of the Invention

The present invention relates to a semiconductor laser device, more in detail to the semiconductor laser device having excellent lasing characteristics and high reliability, for example, most suitable for a semiconductor laser device of a 980 nm band, and a method of fabricating the same.

(b) Description of the Related Art

A semiconductor laser device of a 980 nm band is attracting the public attention as a higher output-power semiconductor laser device used as an excitation optical source in an erbium-doped fiber amplifier (EDFA) system, and the research and development thereof are progressing.

In the step of fabricating the semiconductor laser device including the internal stripe structure, after an n-type current blocking layer is etched to a striped structure, a p-type cladding layer is formed thereon.

It is quite important in fabricating the

semiconductor laser device having excellent lasing characteristics that the etching depth is strictly controlled to etch only a layer to be etched (etched layer), thereby not over-etching the layer underlying the etched
5 layer.

Although the etching depth is ordinarily controlled by adjusting an etching rate or a length of etching time, the accurate control is difficult. Accordingly, an etch stopper is frequently formed underlying the etched layer
10 to automatically stop the progress of the etching at the surface of the etch stopper.

The etch stopper having etching selectivity is made of a material having an etching rate with respect to an etching gas or an etchant significantly lower than that of
15 the etched layer. The formation of the etch stopper under the etched layer automatically stops the progress of the etching because of the difference of the etching rates.

The etch stopper is indispensable in the fabrication
20 of the semiconductor laser device requiring the strict control of the etching depth of the current blocking layer, especially of a higher output self-aligned structure (SAS) semiconductor laser device of the 980 nm band used as an excitation optical source of the EDFA system.

25 The 980 nm band semiconductor laser device

includes an InGaAs layer as an active layer, and an AlGaAs layer as a cladding layer overlying an n-GaAs substrate.

In practice, after the n-AlGaAs cladding layer, the
 5 InGaAs active layer, a p-AlGaAs cladding layer, an n-AlGaAs current blocking layer and an n-GaAs cap layer are stacked, the n-AlGaAs current blocking layer and the n-GaAs cap layer are stacked overlying a GaAs substrate, the n-GaAs cap layer and the n-AlGaAs current blocking
 10 layer are etched to expose the p-AlGaAs cladding layer. In this process, an $\text{In}_{0.49}\text{Ga}_{0.51}\text{P}$ layer is used as the etch stopper having the etching selectively with respect to the AlGaAs layer and being lattice-matched with the GaAs substrate.

15 The use of an SCH layer is proposed in which the layers made of non-doped AlGaAs is disposed between the n-AlGaAs cladding and the InGaAs active layer and between the active layer and the p-AlGaAs cladding layer depending on necessity.

20 A two-layered etch stopper is also proposed as an alternative.

Then, the fabrication of the 980 nm band semiconductor laser device having a two-layered etch stopper will be described.

25 A first $\text{n-Al}_{0.3}\text{Ga}_{0.7}\text{As}$ cladding layer, a first

$\text{Al}_{0.2}\text{Ga}_{0.8}\text{As}$ SCH layer, a first $\text{GaAs}_{0.9}\text{P}_{0.1}$ barrier layer, an $\text{In}_{0.2}\text{Ga}_{0.8}\text{As}$ active layer, a second $\text{GaAs}_{0.9}\text{P}_{0.1}$ barrier layer, and a second $\text{Al}_{0.2}\text{Ga}_{0.8}\text{As}$ SCH layer are sequentially stacked on an n-GaAs substrate.

5 Further, on the second $\text{Al}_{0.2}\text{Ga}_{0.8}\text{As}$ SCH layer are sequentially stacked a second p- $\text{Al}_{0.3}\text{Ga}_{0.7}\text{As}$ cladding layer, a first p-GaAs etch stop layer having a thickness of 10 nm, a second n- $\text{In}_{0.49}\text{Ga}_{0.51}\text{P}$ etch stop layer having a thickness of 10nm, an n- $\text{Al}_{0.35}\text{Ga}_{0.65}\text{As}$ current blocking
10 layer having a thickness of 200 nm, and a p-GaAs cap layer.

In order to obtain the semiconductor laser device, only the n- $\text{Al}_{0.35}\text{Ga}_{0.65}\text{As}$ current blocking layer and the p-GaAs cap layer should be etched to form a stripe.
15 Accordingly, an etching mask is deposited for etching the two layers.

Subsequently, the p-GaAs cap layer and the n- $\text{Al}_{0.35}\text{Ga}_{0.65}\text{As}$ current blocking layer are etched using an etching solution containing an etchant such as tartaric
20 acid and citric acid having etching selectivity with respect to the second n- $\text{In}_{0.49}\text{Ga}_{0.51}\text{P}$ etch stop layer. After the stop of the etching by using the second etch stop layer, the etching mask is removed.

Then, the second etch stop layer is removed by
25 using a hydrochloric acid-based etchant ($\text{HCl} : \text{H}_2\text{PO}_4 = 1 :$

3) having etching selectivity with respect to the first etch stop layer. The progress of the etching is stopped at the first etch stop layer.

Thereafter, the p-Al_{0.3}Ga_{0.7}As cladding layer and the GaAs cap layer are re-grown on the p-GaAs cap layer and the first etch stop layer, thereby fabricating the 980 nm band semiconductor laser device.

However, in this conventional method, an intermediate layer is formed between the second etch stop layer and the current blocking layer. The composition of the intermediate layer different from both of the second etch stop layer and the current blocking layer reduces the durability of the second etch stop layer and exposes the n-Al_{0.3}Ga_{0.7}As cladding layer to the re-grown surface. Consequently, the operation voltage and the threshold current are significantly increased.

The lasing characteristics and the reliability are not satisfactory on the practical basis, and further improvements have been desired.

The above semiconductor laser device had a stripe width of 2.5 μ m, a cavity length of 1200 μ m, an anti-reflection(AR) film having a reflection rate of 5 % formed on the front cleavage facet, and higher reflection (HR) films having a reflection rate of 92 % formed on the front and the rear cleavage facets. When the durability of the

second etch stop layer was maintained and the etching stopped at the surface of the second etch stop layer during the fabrication of the semiconductor laser device having the above configuration by etching the cap layer and the current blocking layer, the threshold current was 17 mA, and the operation voltage was 1.4 V in case of the injected current of 100 mA.

When the durability of the second etch stop layer of the same semiconductor laser device was deteriorated to expose the $n\text{-Al}_{0.3}\text{Ga}_{0.7}\text{As}$ cladding layer to the re-grown surface, the threshold current increased to 40 to 100 mA, and the operation voltage increased to 1.7 to 2.0 V in case of the injected current of 100 mA.

The occurrence of the problem is not restricted to the two-layered etch stop layer described above and to the fabrication of the 980 nm band semiconductor laser device. The problem may arise between any etched layer and any etch stop layer.

The compound semiconductor layer of the semiconductor laser device is frequently etched to provide a desired structure. For example, the top section of the stacked structure including the compound semiconductor layer is etched for ridge formation, and a single compound semiconductor layer is etched for aperture formation. In both cases, the same problem arises when

the etch stop layer is disposed under the etched layer.

SUMMARY OF THE INVENTION

In one aspect of the present invention, a method is
5 provided including the steps of: consecutively depositing
a first etch stop layer, a first compound semiconductor
and a second compound semiconductor layer overlying a
semiconductor substrate, the first etch stop layer, the
first and second compound semiconductor layers having
10 different compositions from one another, etching the first
and second compound semiconductor layers until the
etching stops at the first etch stop layer, and forming a
semiconductor laser device including the first etch stop
layer and the first and second compound semiconductor
15 layers.

In another aspect of the present invention, a
semiconductor laser device is provided which is
fabricated by the above method.

In accordance with these aspects of the invention,
20 the existence of the first compound semiconductor layer
made of a material different from those of the second
compound semiconductor layer and the etch stop layer
and disposing therebetween enables the etching of the
second compound semiconductor layer while controlling
25 the etching depth thereof by using the etch stop layer,

thereby increasing the anti-etching durability of the etch stop layer and the etching accuracy to realize the semiconductor laser device having the excellent lasing characteristics and reliability.

5 The above and other objects, features and advantages of the present invention will be more apparent from the following description.

BRIEF DESCRIPTION OF DRAWINGS

10 Figs.1A and 1B are schematic vertical sectional views showing a conventional method of fabricating a semiconductor laser device and a drawback in the method, respectively.

15 Fig.2 is a vertical sectional view showing a semiconductor laser device in accordance with an embodiment of the present invention.

 Figs.3A to 3C are vertical sectional views sequentially showing a method for fabricating the semiconductor laser device of Fig.2.

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PREFERRED EMBODIMENTS OF THE INVENTION

 The present inventors have investigated the reasons of the unsatisfactory lasing characteristics and reliability of the conventional 980 nm band semiconductor laser
25 device fabricated in accordance with the conventional

method, thereby discovering the following results.

As shown in Fig.1A showing a schematic sectional view of the conventional semiconductor laser device, when an $n\text{-Al}_{0.35}\text{Ga}_{0.65}\text{As}$ current blocking layer 32 is grown on an $\text{In}_{0.49}\text{Ga}_{0.51}\text{P}$ second etch stop layer 30, an intermediate layer 31 is generated at the interface thereof having a complicated and non-uniform composition containing the atoms of the second etch stop layer 30 and the current blocking layer 32.

When the current blocking layer 32 is ideally grown on the second etch stop layer 30 or, for example, the current blocking layer 32 is grown in experimentally ideal conditions, the intermediate layer 31 is not grown. However, in the actual growing step, the intermediate layer 31 is grown made of the atoms complicatedly mixed when the current blocking layer 32 is grown at a practical speed by using the MOCVD method.

Since the etching rate is also non-uniform in the intermediate layer 31, the surface of the $p\text{-In}_{0.49}\text{Ga}_{0.51}\text{P}$ second etch stop layer 30 is non-uniformly etched to make an uneven surface when the $n\text{-Al}_{0.35}\text{Ga}_{0.65}\text{As}$ current blocking layer 32 is etched.

Accordingly, as shown in Fig.1B, when the second etch stop layer 30 is as thin as below 10 nm, partial apertures 29 are perforated in the second etch stop layer

30 such that the p-GaAs first etch stop layer 28 and the n-Al_{0.3}Ga_{0.7}As cladding layer 26 are also etched.

Further, when the n-Al_{0.3}Ga_{0.7}As cladding layer 26 or the Al-containing material is exposed to the re-grown surface, the crystal defects are generated due to the surface oxidation of the Al-containing material during the re-growing of a p-Al_{0.3}Ga_{0.7}As cladding layer. Accordingly, the laser characteristics and the reliability of the semiconductor laser device are lowered.

For example, after the p-Al_{0.3}Ga_{0.7}As cladding layer was re-grown on the p-Al_{0.3}Ga_{0.7}As cladding layer 26 to fabricate the semiconductor laser device, the lasing characteristics were evaluated. As a result, an operation voltage and a threshold voltage were remarkably increased.

The intermediate layer formed at the interface between the current blocking layer 32 and the second etch stop layer 30 causes a problem that the second etch stop layer 30 around the intermediate layer is difficult to be locally removed.

The repeated experiments by the inventors have reached to the present invention under the conception that a compound semiconductor layer such as a GaAs layer made of a material different from those of the current blocking layer 32 and the second etch stop layer

30 is inserted therebetween such that the current blocking layer 32 and the second etch stop layer 30 are separated from each other to prevent the formation of the intermediate layer, thereby increasing the anti-etching durability of the second etch stop layer 30.

In the present invention, the film thicknesses of the second compound semiconductor layer and the etch stop layer are not restricted, and the film thickness of the first compound semiconductor layer is preferably 0.3 nm or more. The effect of the invention cannot be satisfactorily obtained at the thickness below 0.3 nm.

The materials of the first compound semiconductor layer, the second compound semiconductor layer and the etch stop layer are different among one another, and the first compound semiconductor layer is lattice-matched with the substrate.

In a preferred embodiment of the 980 nm semiconductor laser device including the combination of the AlGaAs etched layer and the InGaP etch stop layer, the second compound semiconductor layer includes aluminum, the etch stop layer is a compound semiconductor layer including at least phosphorus, and the first compound semiconductor layer includes neither of aluminum nor phosphorus.

The present invention may be suitably applied to

the semiconductor laser device in which the above intermediate layer is liable to be generated between the etched layer and the etch stop layer.

5 Then, the configuration of a semiconductor laser device of an embodiment in accordance with the second invention will be described referring to Fig.2.

As shown in Fig.2, a semiconductor laser device 50 of the present embodiment has a stacked layer including
 10 a first $n\text{-Al}_{0.3}\text{Ga}_{0.7}\text{As}$ cladding layer 14, a first $\text{Al}_{0.2}\text{Ga}_{0.8}\text{As}$ SCH layer 16, a first $\text{GaAs}_{0.9}\text{P}_{0.1}$ barrier layer 18, an $\text{In}_{0.2}\text{Ga}_{0.8}\text{As}$ active layer 20, a second $\text{GaAs}_{0.9}\text{P}_{0.1}$ barrier layer 22, and a second $\text{Al}_{0.2}\text{Ga}_{0.8}\text{As}$ SCH layer 24 sequentially stacked on an $n\text{-GaAs}$ substrate 12,
 15 similarly to the conventional semiconductor laser device.

Further, on the second $\text{Al}_{0.2}\text{Ga}_{0.8}\text{As}$ SCH layer 24 are sequentially stacked a second $p\text{-Al}_{0.3}\text{Ga}_{0.7}\text{As}$ cladding layer 26, a first $p\text{-GaAs}$ etch stop layer 28 having a thickness of 10 nm, a second $n\text{-In}_{0.49}\text{Ga}_{0.51}\text{P}$ etch stop layer
 20 30 having a thickness of 10nm, an $n\text{-Al}_{0.35}\text{Ga}_{0.65}\text{As}$ current blocking layer 32 having a thickness of 200 nm, and a $p\text{-GaAs}$ cap layer 34, and different from the conventional semiconductor laser device, an $n\text{-GaAs}$ layer 52 having a thickness of 5 nm is disposed between the second etch
 25 stop layer 30 and the current blocking layer 32.

A p-type electrode 54 is formed on the p-GaAs cap layer 34, and an n-type electrode 56 is formed on the bottom surface of the n-GaAs substrate 12.

Since the existence of the n-GaAs layer 52 separates
 5 the n-Al_{0.35}Ga_{0.65}As current blocking layer 32 and the second n-In_{0.49}Ga_{0.51}P etch stop layer 30 from each other, the interface heretofore formed is not substantially formed between the current blocking layer 32 and the second etch stop layer 30. Accordingly, the intermediate
 10 layer made of the variety of atoms non-uniformly mixed is not formed.

The absence of the intermediate layer could be confirmed by using a transmission electron microscope, and the operation voltage and the threshold voltage of the
 15 semiconductor laser device 50 of the embodiment were significantly decreased in comparison with those of the conventional semiconductor laser device including no n-GaAs layer.

20 Then, a method for fabricating the semiconductor laser device of the embodiment will be described referring to Figs.3A to 3C.

As shown in Fig.3A, the first n-Al_{0.3}Ga_{0.7}As cladding layer 14, the first Al_{0.2}Ga_{0.8}As SCH layer 16, the first
 25 GaAs_{0.9}P_{0.1} barrier layer 18, the In_{0.2}Ga_{0.8}As active layer

20, the second $\text{GaAs}_{0.9}\text{P}_{0.1}$ barrier layer 22, and the second $\text{Al}_{0.2}\text{Ga}_{0.8}\text{As}$ SCH layer 24 are sequentially stacked on the n-GaAs substrate 12, similarly to the conventional semiconductor laser device.

5 Further, on the second $\text{Al}_{0.2}\text{Ga}_{0.8}\text{As}$ SCH layer 24 are sequentially stacked the second p- $\text{Al}_{0.3}\text{Ga}_{0.7}\text{As}$ cladding layer 26, the first p-GaAs etch stop layer 28 having the thickness of 10 nm, the second n- $\text{In}_{0.49}\text{Ga}_{0.51}\text{P}$ etch stop layer 30 having the thickness of 10nm, the GaAs layer 52
10 having a thickness of 5 nm, the n- $\text{Al}_{0.35}\text{Ga}_{0.65}\text{As}$ current blocking layer 32 having the thickness of 200 nm, and the p-GaAs cap layer 34.

Then, as shown in Fig.3A, an etching mask 36 is deposited for etching the p-GaAs cap layer 34 and the n-
15 $\text{Al}_{0.35}\text{Ga}_{0.65}\text{As}$ current blocking layer 32.

Subsequently, as shown in Fig.3B, the p-GaAs cap layer 34, the n- $\text{Al}_{0.35}\text{Ga}_{0.65}\text{As}$ current blocking layer 32 and the n-GaAs layer 52 are etched using an etching solution containing an etchant such as tartaric acid and citric acid
20 having etching selectivity with respect to the second n- $\text{In}_{0.49}\text{Ga}_{0.51}\text{P}$ etch stop layer 30. After the stop of the etching by using the second etch stop layer 30, the etching mask is removed.

Then, as shown in Fig.3C, the second etch stop layer
25 30 is removed by using a hydrochloric acid-based etchant

(HCl : H₂PO₄ = 1 : 3) having etching selectivity with respect to the first etch stop layer 28.

Thereafter, the p-Al_{0.3}Ga_{0.7}As cladding layer 38 and the GaAs cap layer 34 are re-grown on the p-GaAs cap layer 34 and the exposed first etch stop layer 28. The resulting structure is referred to as the self-aligned structure (SAS).

Then, the p-type electrode 54 is formed on the p-GaAs cap layer 34, and the n-type electrode 56 is formed on the bottom surface of the n-GaAs substrate 12, thereby providing the 980 nm band semiconductor laser device structure 50.

Also in the semiconductor laser device fabricated by the above method, the interface is not formed for preventing the formation of the intermediate layer.

The second etch stop layer 30 is not hardly present having the uniform thickness on the entire layer during the etching of the current blocking layer because the formation of the intermediate layer prevents the local sway of the second etch stop layer 30. Therefore, the anti-etch stopping durability of the second etch stop layer 30 increases, and even if the thickness of the second etch stop layer 30 is 10 nm or less, the apertures are not locally perforated due to the etching.

The increase of the anti-etch stopping durability,

and the formation of the re-grown surface without surface roughness could be confirmed by using a reflection electron microscope.

The prevention of the intermediate layer formation by the insertion of the n-GaAs layer 52 does not arise a conventional problem that the smooth etching of the second etch stop layer 30 is hardly conducted by the intermediate layer formation.

The lasing characteristics of the semiconductor laser device fabricated in this manner were measured. As a result, the operation voltage and the threshold voltage were remarkably decreased compared with the conventional semiconductor laser device in which the $\text{Al}_{0.3}\text{Ga}_{0.7}\text{As}$ cladding layer is exposed due to the absence of the GaAs layer on the surface thereof.

The present embodiment could be applied to other combinations than the combination of the AlGaAs etched layer and the InGaP etch stop layer.

Since the above embodiment is described only for examples, the present invention is not limited to the above embodiment and various modifications or alterations can be easily made therefrom by those skilled in the art without departing from the scope of the present invention.